DBC0062F1V51-1

TWIN DIPLEXER 1900-AWS/WCS

Kaelus world-proven outdoor solutions provide strong RF performance coupled with rugged operation. The DBC0062 provides the following features:



FEATURES

- Enables feeder sharing of various technologies including 1900, AWS, WCS, 2300,and 2600
- low insertion loss and high isolation
- Field configurable DC switch.
- Low passive IM performance

TECHNICAL SPECIFICATIONS

PORT NAME	1710 - 2170	2302 - 2690
Passband	1710 - 2170MHz	2302 - 2690MHz
Insertion loss	0.15dB typical	
Return loss	18dB minimum	
Maximum input power	200W (average) / 2kW (PEP)	200W (average) / 2kW (PEP)
Attenuation	50dB minimum	

ELECTRICAL	
Impedance	50Ohms
Intermodulation products	-153dBc maximum all ports with 2 x 20W carriers

DC / AISG	
A field selectable DC/AISG path is achieved via mechanical links on a IEC60130-9, 8-pin female connector (see electrical block diagram).	
Passband	0 - 3MHz
Insertion loss, single DC/AISG pass	1dB maximum
Return loss, single DC/AISG pass	12dB minimum
Insertion loss, two DC/AISG pass	4.5dB typical
Return loss, two DC/AISG pass	9dB typical
Input voltage range	+ 31V
DC current rating	2A continuous, 4A peak

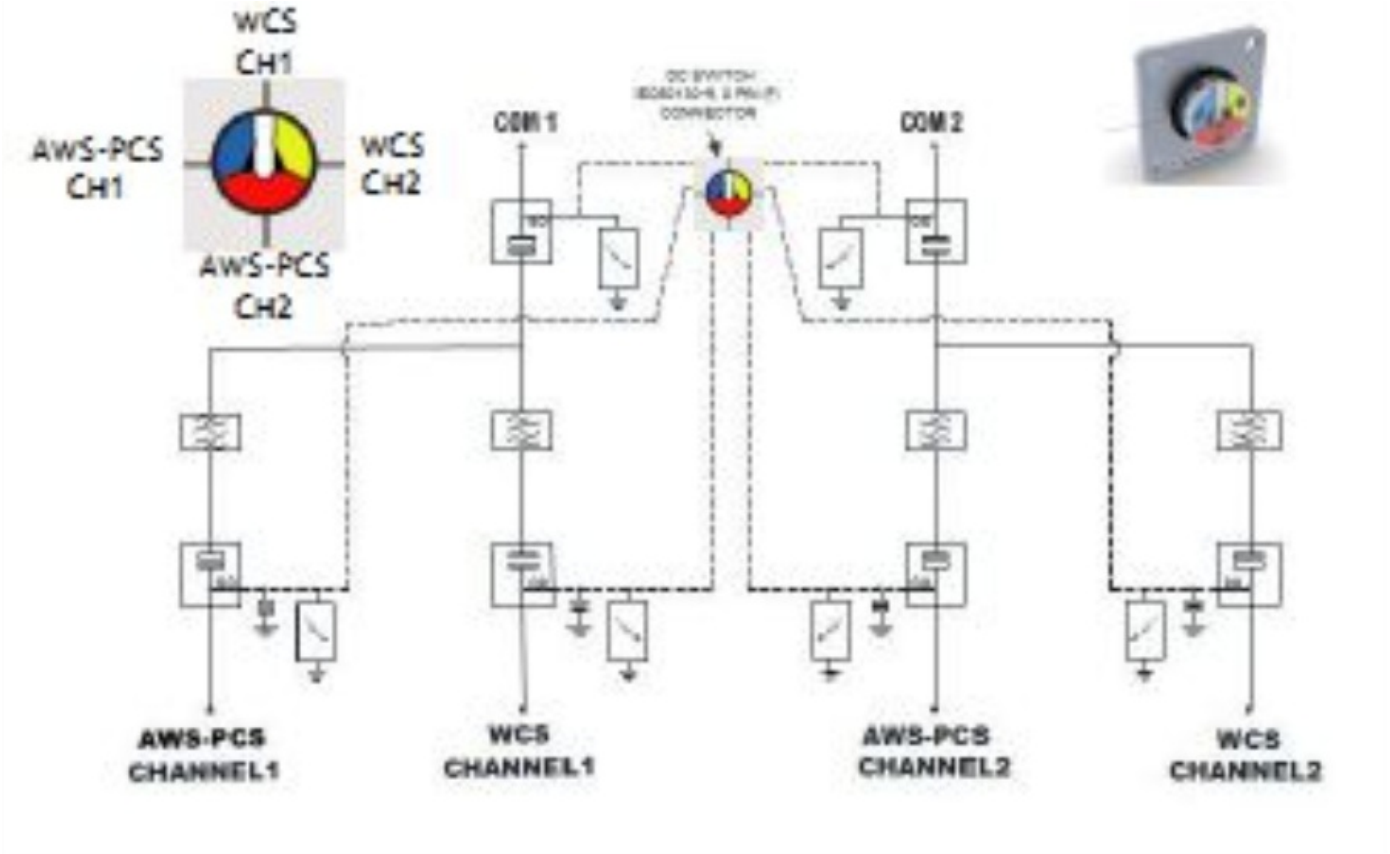
ENVIRONMENTAL	
For further details of environmental compliance, please contact Kaelus.	
Temperature range	-20°C to +65°C | -4°F to +149°F
Ingress protection	IP67
Lightning protection	RF: ±5kA maximum (8/20us)
MTBF	>1,000,000 hours
Compliance	ETSI EN 300 019 class 4.1, RoHS

MECHANICAL	
Dimensions H x D x W	246 x 223 x 66mm | 9.7 x 8.8 x 2.6in
Weight	3kg | 6.6lbs estimated
Finish	Painted, light grey (RAL7035)
Connectors	DIN 7-16 (F) x 6 long neck
Mounting	Pole/wall bracket supplied with two metal clamps for 45-178 mm diameter poles

ORDERING INFORMATION

PART NUMBER	DESCRIPTION
DBC0062F1V51-1	Twin Diplexer 1900-AWS/WCS, With external DC switch
DBC0062F1V1-1	Twin Diplexer 1900-AWS/WCS, No DC through
DBC0062F1V2-1	Twin Diplexer 1900-AWS/WCS, DC/AISG path from common to 1900-AWS port
DBC0062F1Vxx-1	Various DC/AISG configuration available

ELECTRICAL BLOCK DIAGRAM



MECHANICAL BLOCK DIAGRAM

